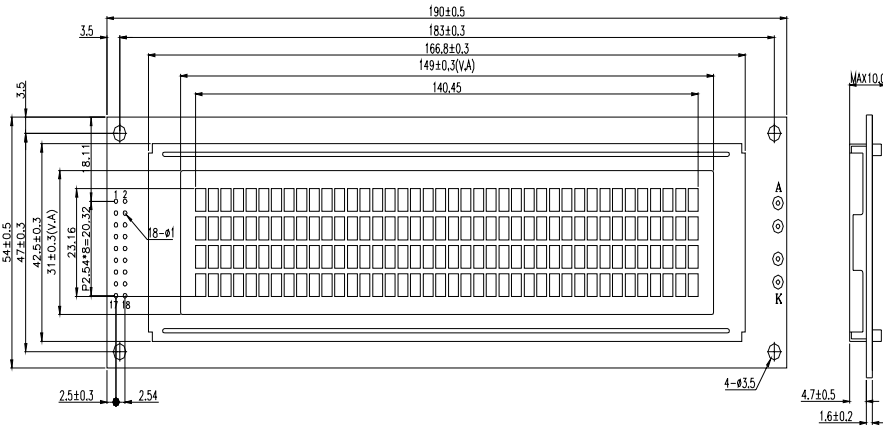


**OUTLINE DIMENSION**



Unmarked Tolerance: 0.5mm

**Display Data Address**

Character	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
Line 1	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	20	21	22	23
Line 2	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
Line 3	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	20	21	22	23
Line 4	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
Character	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
Line 1	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
Line 2	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67
Line 3	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
Line 4	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67

**NOTES:**

1. DISPLAY TYPE: STN Y-G, TRASFLECTIVE/POSITIVE.
2. DRIVE: KS0066U OR EQUAL
3. VIEWING ANGLE: 6 O' CLOCK.
4. PIN CONNECTION:

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	E1 R/W	RS	V0 VSS	Vcc	E2	NC	Vled	Vlss		

5. OPERATING TEMP: -20~70 .
6. STORAGE TEMP -30~80 .

**OPERATING OPTIO**

**Input signal Function**

No.	Symbol	Function	Note
1-8	DB7~DB0	Data Bus Line	
9	E1	Enable signal	
10	R/W	Read/Write select	
11	RS	Data/Instruction select Power	
12	V0	Supply for Driving the LCD	
13	VSS	Ground (0V)	
14	VCC	Power Supply for Logic Circuit	
15	E2	Enable signal	
16	NC	NONE	
17	Vled	LED+	
18	Vlss	LED-	

**Block diagram**

