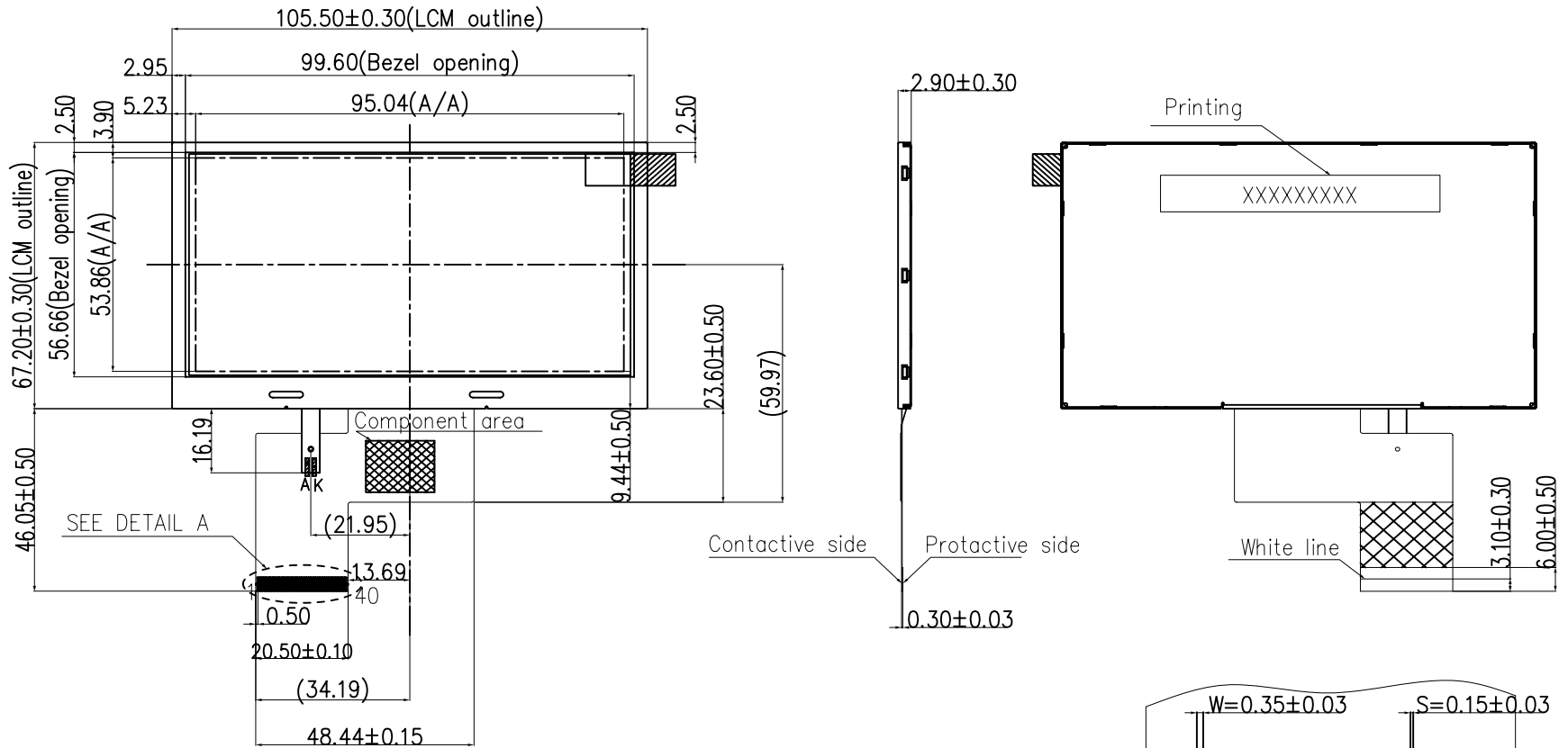




| NO. | PIN NAME |
|-----|----------|
| 1 | VLED- |
| 2 | VLED+ |
| 3 | GND |
| 4 | VDD |
| 5 | R0 |
| 6 | R1 |
| 7 | R2 |
| 8 | R3 |
| 9 | R4 |
| 10 | R5 |
| 11 | R6 |
| 12 | R7 |
| 13 | G0 |
| 14 | G1 |
| 15 | G2 |
| 16 | G3 |
| 17 | G4 |
| 18 | G5 |
| 19 | G6 |
| 20 | G7 |
| 21 | B0 |
| 22 | B1 |
| 23 | B2 |
| 24 | B3 |
| 25 | B4 |
| 26 | B5 |
| 27 | B6 |
| 28 | B7 |
| 29 | GND |
| 30 | DCLK |
| 31 | DISP |
| 32 | HSYNC |
| 33 | VSYNC |
| 34 | DE |
| 35 | NC |
| 36 | GND |
| 37 | X_R |
| 38 | Y_B |
| 39 | X_L |
| 40 | Y_T |



NOTES:

1. Display: TFT;
2. Gray Scale Inversion:
3. General Tolerance: ± 0.20 ;
4. Driver IC:
5. Opening Temperature: $-20^{\circ}\text{C}-70^{\circ}\text{C}$;
Storage Temperature: $-30^{\circ}\text{C}-80^{\circ}\text{C}$;
6. Requirements on Environment Protection: Q/S0002.
7. Recommended Case Open Area should be less than Module V.A

