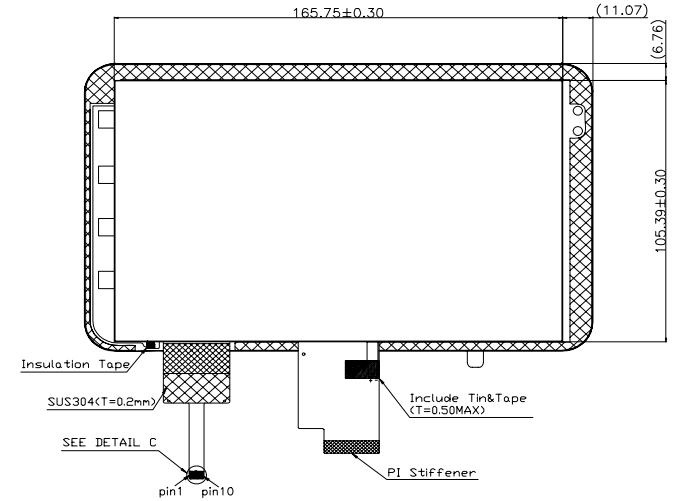
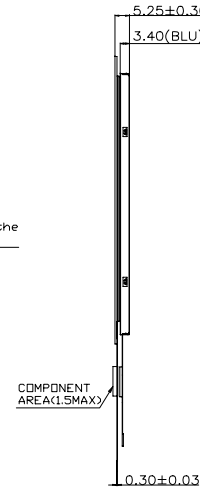
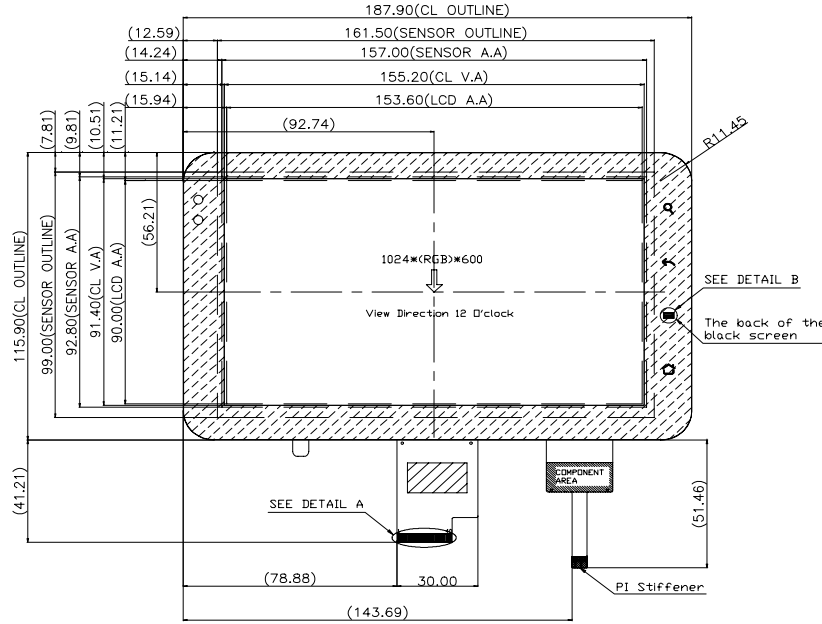
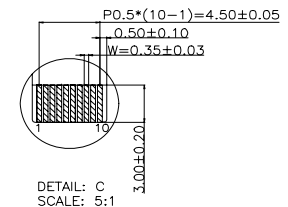
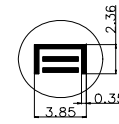
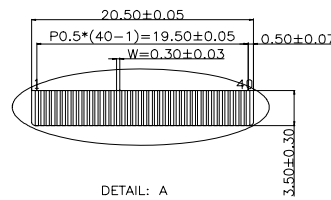
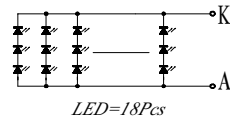




NO.	PIN NAME
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	VDD
29	AVDD
30	GND
31	LED-
32	LED-
33	LED-
34	VGL
35	NC
36	NC
37	VGH
38	LED+
39	LED+
40	LED+



CIRCUIT DIAGRAM



TSP pin assignment

Pin No.	Assignment
1	SCL
2	SDA
3	GND
4	GND
5	INT
6	GND
7	RESET
8	VDD
9	GND
10	GND

NOTES:

1. Display: TFT;
2. Backlight: 18 chips LED;
3. Gray Scale Inversion: 6 o'clock;
4. General Tolerance: ±0.20;
5. Requirement on environment protection: RoHS;
6. Recommended Case Open Area should be less than Module V.A
7. Recommended Cushion adherent area: TP V.A+1.6mm